

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,830,961 B2
APPLICATION NO. : 09/918739
DATED : December 14, 2004
INVENTOR(S) : David J. Corisis

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

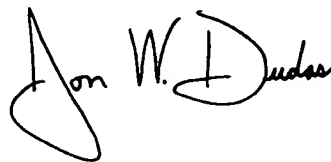
COLUMN 4, LINE 63,	change "the first" to --the second--
COLUMN 5, LINE 24,	change "semiconductor 200" to --
	semiconductor 100--
COLUMN 6, LINE 34,	change "the first plurality" to --the second
	plurality--
COLUMN 7, LINE 51,	change "the first plurality" to --the second
	plurality--

In the claims:

CLAIM 1 COLUMN 9, LINE 4,	change "a the" to --of the--
CLAIM 1 COLUMN 9, LINE 12,	change "adjacent a" to --adjacent the--
CLAIM 4 COLUMN 9, LINE 25,	change "including the step of:" to --including:--
CLAIM 5 COLUMN 9, LINE 27,	change "including the step of:" to --including:--
CLAIM 9 COLUMN 10, LINE 7,	change "an end" to --the end--
CLAIM 10 COLUMN 10, LINE 9,	change "the at least one" to --each--
CLAIM 11 COLUMN 10, LINE 20,	change "an end" to --the end--
CLAIM 11 COLUMN 10, LINE 22,	change "beyond a" to --beyond the--
CLAIM 12 COLUMN 10, LINE 29,	change "an end" to --the end--
CLAIM 12 COLUMN 10, LINE 31,	change "an end" to --the end--
CLAIM 12 COLUMN 10, LINE 34,	change "beyond a" to --beyond the--

Signed and Sealed this

Fifteenth Day of January, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office